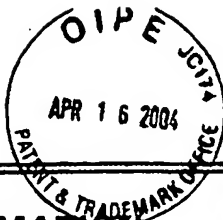


1/29/04

SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449		<b>CUSTOMER NUMBER</b>  26615	ATTORNEY'S DKT No. H1504		APPLICATION No. 10/726,569		
			APPLICANT(S) Haihong WANG et al.				
			FILING DATE December 4, 2003		GROUP Unassigned		
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE	
<i>ML</i>	6,492,212 B1	12-10-02	leong et al.	438	157		
	6,537,880 B1	3-25-03	Tseng	438	260		
	6,358,827 B1	3-19-02	Chen et al.	438	585		
	6,514,819 B1	2-4-03	Choi	438	253		
	6,413,802 B1	7-2-02	Hu et al.	438	151		
	5,705,414	1-6-98	Lustig	438	585		
	5,932,911	8-3-99	Yue et al.	257	330		
	6,180,441	1-30-01	Yue et al.	438	197		
	6,562,665	05-2003	Yu, Bin	438	149		
	4,996,574	02-1991	Shirasaki, Masahiro	257	66		
	5,338,959	08-1994	Kim et al.	257	331		
	<i>ML</i>	6,583,469	06-2003	Fried et al.	257	329	
<b>FOREIGN PATENT DOCUMENTS</b>							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>							
<i>ML</i>	Copy of U.S. Serial No. <u>10/754,515</u> and Preliminary Amendment; filed January 12, 2004; entitled: "FINFET DEVICE WITH MULTIPLE FIN STRUCTURES"; 20 pages.						
<i>ML</i>	Copy of U.S. Serial No. <u>10/755,344</u> and Preliminary Amendment; filed January 13, 2004; entitled: "FINFET DEVICE WITH MULTIPLE CHANNELS"; 24 pages.						
<i>ML</i>	United States Patent Application Publication No. US <u>2003-0178677 A1</u> ; Publication Date: September 25, 2003; Clark et al.						
EXAMINER <i>Aduskyen</i>			DATE CONSIDERED <i>9/26/05</i>				

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4/16/09

SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449	<b>CUSTOMER NUMBER</b>  26615	ATTORNEY'S DKT No. H1504	APPLICATION No. 10/726,569
		APPLICANT(S) Haihong WANG et al.	
		FILING DATE December 4, 2003	GROUP 2811

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
ML	6,232,622 B1	05/15/2001	Hamada	257	67	
ML	6,645,797 B1	11/11/2003	Buynoski et al.	438	157	

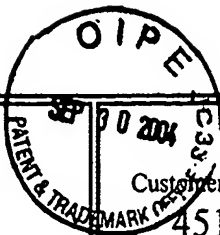
**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**


EXAMINER <i>Michael R. Meyer</i>	DATE CONSIDERED <i>9/26/05</i>
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9/30/09

SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449	Customer Number <b>45114</b>	ATTORNEY'S DKT NO. H1504	APPLICATION No. 10/726,569
		APPLICANT(S) Haihong Wang et al.	
		FILING DATE December 4, 2003	GROUP 2811

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>plc</i>	6,716,686 B1	04-06-04	Buynoski et al.	438	157	07-08-03
	6,525,403 B2	02-25-03	Inaba et al.	257	618	09-24-01
	2004/0099885 A1	05-27-04	Yeo et al.	257	208	11-26-02
	2004/0036126 A1	02-26-04	Chau et al.	257	401	08-23-02
<i>plc</i>	2002/0011612 A1	01-31-02	Hieda	257	262	07-30-01

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**


EXAMINER <i>Delia S. Kuyper</i>	DATE CONSIDERED <i>9/26/05</i>
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OCT 18 2004

SHEET 1 OF 1

**INFORMATION  
DISCLOSURE  
CITATION**  
PTO-1449

Customer Number  
**45114**

ATTORNEY'S DKT No.  
H1504

APPLICATION No.  
10/726,559

APPLICANT(S)  
Haihong Wang et al.

FILING DATE  
December 4, 2003

GROUP  
2811

**U.S. PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>ML</i>	6,794,718 B2	09-21-04	Nowak et al.	257	347	12-19-02
	6,770,516 B2	08-03-04	Wu et al.	438	154	09-05-02
	6,657,259 B2	12-02-03	Fried et al.	257	350	12-04-01
	6,537,880 B1	03-25-03	Tseng	438	260	09-13-01
	6,177,299 B1	01-23-01	Hsu et al.	438	149	01-15-98
	5,545,586	08-13-96	Koh	437	89	05-13-94
	5,115,289	05-19-92	Hisamoto et al.	357	23.7	08-05-91
	2002/0060338 A1	05-23-02	Zhang	257	328	11-06-01
<i>ML</i>	2002/0043690 A1	04-18-02	Doyle et al.	257	368	03-14-00

**FOREIGN PATENT DOCUMENTS**

EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No
<i>ML</i>	EP 1 383 164 A1	01-21-04	EPO				
	EP 1 202 335 A2	05-02-02	EPO				
<i>ML</i>	DE 196 33 914 C1	08-07-97	Germany				

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)**


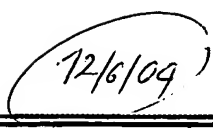

EXAMINER

*Delia Kasper*

DATE CONSIDERED

*9/26/05*

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<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449	Customer Number <b>45114</b>	ATTORNEY'S DKT NO. H1504	APPLICATION NO. 10/726,569
		APPLICANT(S) Haihong Wang et al.	
		FILING DATE December 4, 2003	GROUP 2811

U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
<i>dlr</i>	6,475,869 B1	11-05-02	Yu	438	303	02-26-01
	6,063,688	05-16-00	Doyle et al.	438	424	09-29-97
	2002/0153587 A1	10-24-02	Adkisson et al.	257	510	07-02-02
<i>dlr</i>	2001/0005022 A1	06-28-01	Ogura	257	103	12-21-00

FOREIGN PATENT DOCUMENTS							
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						Yes	No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)	

EXAMINER <i>Aduskyan</i>	DATE CONSIDERED <i>9/26/05</i>
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12/4/03

SHEET 1 OF 1

<b>INFORMATION DISCLOSURE CITATION</b> PTO-1449		Customer Number <b>26615</b>	ATTORNEY'S DKT No. H1504		APPLICATION No. Unassigned	
			APPLICANT(S) Haihong WANG et al.			
			FILING DATE December 4, 2003		GROUP Unassigned	
U.S. PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
FOREIGN PATENT DOCUMENTS						
EXAMINER'S INITIALS	PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation Yes    No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)						
<i>He</i>	Digh Hisamoto et al., "FinFET-A Self-Aligned Double-Gate MOSFET Scalable to 20 nm," IEEE Transactions on Electron Devices, Vol. 47, No. 12, December 2000, pages 2320-2325.					
<i>He</i>	Yang-Kyu Choi et al., "Sub-20nm CMOS FinFET Technologies," 2001 IEEE, IEDM, pages 421-424.					
<i>He</i>	Xuejue Huang et al., "Sub-50 nm P-Channel FinFET," IEEE Transactions on Electron Devices, Vol. 48, No. 5, May 2001, pages 880-886.					
<i>He</i>	Xuejue Huang et al., "Sub 50-nm FinFET: PMOS," 1999 IEEE, IEDM, pages 67-70.					
<i>He</i>	Yang-Kyu Choi et al., "Nanoscale CMOS Spacer FinFET for the Terabit Era," IEEE Electron Device Letters, Vol. 23, No. 1, January 2002, pages 25-27.					
<i>He</i>	Copy of U.S. Serial No. 10/405,343; filed April 3, 2003; entitled: "METHOD FOR FORMING STRUCTURES IN FINFET DEVICES"; 31 pages.					
EXAMINER			DATE CONSIDERED			
<i>Richard K. Meyer</i>			<i>9/26/05</i>			

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